Class 00: Class Introduction

EE584 Introduction of VLSI Design and Testing

Syllabus:
• 40% Two Exams
• 20% Design Project and Report
• 20% Two Design Assignments
• 10% Eight Homeworks
• 10% Five Quizzes
Class 00: Class Introduction

Main Class Topics:
Hands-On Experience using Mentor Graphics
Theoretical Concepts from Lectures

• Chip Process Flow
  • Basic physics of process
• Chip Design Flow
  • Schematics
  • Simulation
  • Layout
  • Verification
• Advanced Topics
  • Various NOVO memories, architecture
Class 00: Class Introduction

• Lecture Notes are available on web
  • http://www.engr.uky.edu/~elias

• Lecture notes are subject to change

• Design Project teams to be N groups of 3 persons each
  • Sign up to be held next Tuesday

• Class email account will be setup by next week
  • Fill out spreadsheet being passed around
Class 00: Class Introduction

Important notes from syllabus
1. Documentation is important
2. Do your own work
3. Do your work ahead of time DO NOT PROCRASTINATE
4. You will have problems with the software
5. Tell myself and the TA when issues arise and the exact order of events that caused the issue

Treat this class as your segue from academia to industry:
⇒ You need to be able to write
⇒ You need to be able to read
⇒ You need to be able to talk
⇒ You need to be able to think
⇒ You do not need to grub for points